

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operat or	Plural s	Time Stamp
L1	657	(375/240.25).CCLS.	USPAT; USOCR	OR	OFF	2007/12/07 22:57
L2	2429	((375/240.25) or (375/240.29) or (345/521,516) or (348/718) or (711/1) or (395/853)).CCLS.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/07 22:59
L3	3	l2 and MPEG and DV adj "25" and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/07 23:01
S1	0	2003/185306	USPAT	OR	OFF	2007/12/07 22:56
S2	0	2003/185306.pn.	USPAT	OR	OFF	2007/12/06 17:04
S3	0	"2003185306".pn.	USPAT	OR	OFF	2007/12/06 17:04
S4	408	macinnis.in.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 17:32
S5	1	("5812760").PN.	USPAT; USOCR	OR	OFF	2007/12/06 17:32
S6	17	("5812760").URPN.	USPAT	OR	OFF	2007/12/06 19:24

EAST Search History

S7	0	MPEG adj (S1 S3) and DV adj "25" and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/07 23:00
S8	16	MPEG and DV near "25" and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 19:38
S9	8	("5237410" "5329311" "5400082" "5610729" "5852475" "5920356" "6064776" "6577679").PN.	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:36
S10	0	("5237410" "5329311" "5400082" "5610729" "5852475" "5920356" "6064776" "6577679").PN. and MPG and DV-25	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:36
S11	0	("5237410" "5329311" "5400082" "5610729" "5852475" "5920356" "6064776" "6577679").PN. and MPG\$3 and DV\$3	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:37
S12	0	("5237410" "5329311" "5400082" "5610729" "5852475" "5920356" "6064776" "6577679").PN. and MPG\$3	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:37
S13	0	("5237410" "5329311" "5400082" "5610729" "5852475" "5920356" "6064776" "6577679").PN. and MPG	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:37

EAST Search History

S14	2	S9 and MPEG	US-PGPU B; USPAT; USOCR	OR	OFF	2007/12/06 19:37
S15	14310	MPEG\$3 and DV\$3 and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:14
S16	1	S9 and MPEG\$3 and DV\$3 and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 19:41
S17	15	MPEG\$3 and DV adj "25" and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:04
S18	1	("5912676").PN.	USPAT; USOCR	OR	OFF	2007/12/06 20:37
S19	1	S18 and mpeg	USPAT	OR	OFF	2007/12/06 20:07
S20	0	S18 and mpeg and dv adj "25"	USPAT	OR	OFF	2007/12/06 20:08
S21	0	S18 and mpeg and dv25	USPAT	OR	OFF	2007/12/06 20:08
S22	0	S18 and mpeg4	USPAT	OR	OFF	2007/12/06 20:08
S23	0	S18 and mpeg-4	USPAT	OR	OFF	2007/12/06 20:08

EAST Search History

S24	0	S18 and mpeg2	USPAT	OR	OFF	2007/12/06 20:09
S25	1	S18 and mpeg-1	USPAT	OR	OFF	2007/12/06 20:09
S26	1	S18 and mpeg-2	USPAT	OR	OFF	2007/12/06 20:10
S27	1	("6961063").PN.	USPAT; USOCR	OR	OFF	2007/12/06 20:10
S28	1	S27 and mpeg	USPAT	OR	OFF	2007/12/06 20:10
S29	0	S27 and mpeg and dv	USPAT	OR	OFF	2007/12/06 20:11
S30	1	S27 and mpeg and dv\$3	USPAT	OR	OFF	2007/12/06 20:11
S31	1	S5 andMPEG\$3 and DV\$3 and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:13
S32	0	S5 and MPEG\$3 and DV\$3 and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:13
S33	1	S18 and MPEG\$3 and DV\$3 and video and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:15
S34	0	("20030185306").PN.	USPAT; USOCR	OR	OFF	2007/12/06 20:28

EAST Search History

S35	2	("20030185306").PN.	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:29
S36	15064	S35 amd mpeg and dv\$4	USPAT	OR	OFF	2007/12/06 21:28
S37	0	S35 and mpeg and dv\$4	USPAT	OR	OFF	2007/12/06 20:29
S38	0	S35 and mpeg	USPAT	OR	OFF	2007/12/06 20:30
S39	0	S35 and mpeg\$2	USPAT	OR	OFF	2007/12/06 20:30
S40	0	S35 and mpeg\$4	USPAT	OR	OFF	2007/12/06 20:31
S41	1	S35 and mpeg\$4	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 21:30
S42	0	S35 and mpeg\$4 and dv\$4	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 20:31
S43	1	S18 and mpeg\$3 and dv\$3	USPAT	OR	OFF	2007/12/06 20:38
S44	1	S18 and mpeg\$3 and dv\$3 and decod\$3 and encod\$3	USPAT	OR	OFF	2007/12/06 21:27

EAST Search History

S45	1	S18 and mpeg\$3 and dv\$3 and decod\$3 and encod\$3 and video	USPAT	OR	OFF	2007/12/06 20:43
S46	1	S35 and mpeg\$4 and decod\$3 and encod\$3	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 21:34
S47	1	S35 and mpeg\$4 and decod\$3 and encod\$3 and instruction and processor	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 21:39
S48	0	S35 and mpeg\$4 and decod\$3 and encod\$3 and instruction and processor and first and scnd and third and fourth	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 21:39
S49	1	S35 and mpeg\$4 and decod\$3 and encod\$3 and instruction and processor and first and second and third and fourth	US-PGPU B; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/12/06 21:40



Day : Friday
Date: 12/7/2007
Time: 22:43:25

Inventor Name Search Result

Your Search was:

Last Name = BIDNUR
First Name = RAVINDRA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10463170	Not Issued	123	06/16/2003	Direct memory accessing for fetching macroblocks	BIDNUR, RAVINDRA
10463315	Not Issued	120	06/17/2003	MPEG smart video transport processor with different transport packet handling	BIDNUR, RAVINDRA
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	BIDNUR, RAVINDRA
11096653	Not Issued	30	04/01/2005	System, method, and apparatus for audio decoding accelerator	BIDNUR, RAVINDRA
60789347	Not Issued	159	04/05/2006	7.2 MbPS 3GPP HSDPA coprocessor with advanced NLMS receiver and receive diversity for mobile terminals	BIDNUR, RAVINDRA NARASIMHA RAO

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Inventor Name Search Result

Your Search was:

Last Name = PAI

First Name = RAMADAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11092932	Not Issued	30	03/29/2005	System, method, and apparatus for AC coefficient prediction	PAI, RAMADAS L.
10735980	7284072	150	12/15/2003	DMA ENGINE FOR FETCHING WORDS IN REVERSE ORDER	PAI, RAMADAS LAKSHMIKANTH
10736125	Not Issued	71	12/15/2003	DMA engine for fetching words in reverse bit order	PAI, RAMADAS LAKSHMIKANTH
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	PAI, RAMADAS LAKSHMIKANTH
10816118	Not Issued	41	04/01/2004	Read/write separation in video request manager	PAI, RAMADAS LAKSHMIKANTH
10816320	Not Issued	41	04/01/2004	Motion vector address computer error detection	PAI, RAMADAS LAKSHMIKANTH
10824897	Not Issued	41	04/15/2004	Video decoder for supporting both single and four motion vector macroblocks	PAI, RAMADAS LAKSHMIKANTH
11096653	Not Issued	30	04/01/2005	System, method, and apparatus for audio decoding accelerator	PAI, RAMADAS LAKSHMIKANTH
60494666	Not Issued	159	08/13/2003	DMA state machine for fetching addresses in reverse order	PAI, RAMADAS LAKSHMIKANTH
60494746	Not Issued	159	08/13/2003	DMA architecture for providing data words in reverse order	PAI, RAMADAS LAKSHMIKANTH

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Inventor Name Search Result

Your Search was:

Last Name = SHERIGAR

First Name = BHASKAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10060118	Not Issued	41	01/29/2002	Error concealment for MPEG decoding with personal video recording functionality	SHERIGAR, BHASKAR
10737492	Not Issued	71	12/16/2003	Dual context audio parser	SHERIGAR, BHASKAR
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	SHERIGAR, BHASKAR
10880695	Not Issued	40	06/29/2004	Huffman decoder used for decoding both advanced audio coding (AAC) and MP3 audio	SHERIGAR, BHASKAR
11092256	Not Issued	30	03/29/2005	System, method, and apparatus for DC coefficient transformation	SHERIGAR, BHASKAR
11092285	7091888	150	03/29/2005	RUN-LEVEL AND COMMAND SPLIT FIFO STORAGE APPROACH IN INVERSE QUANTIZATION	SHERIGAR, BHASKAR
11092339	Not Issued	30	03/29/2005	System, and method for DC coefficient prediction	SHERIGAR, BHASKAR
11092347	Not Issued	30	03/29/2005	Unified architecture for inverse scanning for plurality of scanning scheme	SHERIGAR, BHASKAR
11092932	Not Issued	30	03/29/2005	System, method, and apparatus for AC coefficient prediction	SHERIGAR, BHASKAR
11096604	Not Issued	30	04/01/2005	Hardware implementation of inverse scan for a plurality of standards	SHERIGAR, BHASKAR
11096988	Not Issued	30	04/01/2005	Hardware implementation of optimized single inverse quantization engine for a plurality of standards	SHERIGAR, BHASKAR
11317578	Not Issued	30	12/23/2005	Hardware implementation of programmable controls for inverse quantizing with a plurality of standards	SHERIGAR, BHASKAR
60542401	Not Issued	159	02/05/2004	Huffman decoder used for decoding both advanced audio coding (AAC) and MP3 audio	SHERIGAR, BHASKAR
10735980	7284072	150	12/15/2003	DMA ENGINE FOR FETCHING WORDS IN REVERSE ORDER	SHERIGAR, BHASKAR MALA
10736125	Not Issued	71	12/15/2003	DMA engine for fetching words in reverse bit order	SHERIGAR, BHASKAR MALA

60494666	Not Issued	159	08/13/2003	DMA state machine for fetching addresses in reverse order	SHERIGAR, BHASKAR MALA
60494746	Not Issued	159	08/13/2003	DMA architecture for providing data words in reverse order	SHERIGAR, BHASKAR MALA

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Inventor Name Search Result

Your Search was:

Last Name = SANE

First Name = ANIRUDDHA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10411632	Not Issued	120	04/11/2003	Dynamically booting processor code memory with wait instruction	SANE, ANIRUDDHA
10414580	7174358	150	04/15/2003	SYSTEM, METHOD, AND APPARATUS FOR DIVISION COUPLED WITH TRUNCATION OF SIGNED BINARY NUMBERS	SANE, ANIRUDDHA
10414842	7165086	150	04/15/2003	SYSTEM, METHOD, AND APPARATUS FOR DIVISION COUPLED WITH ROUNDING OF SIGNED BINARY NUMBERS	SANE, ANIRUDDHA
10606145	6867715	150	06/25/2003	SYSTEM, METHOD, AND APPARATUS FOR VARIABLE LENGTH DECODER	SANE, ANIRUDDHA
10765809	Not Issued	30	01/27/2004	Decoding of digital video standard material during variable length decoding	SANE, ANIRUDDHA
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	SANE, ANIRUDDHA
11042221	6995696	150	01/25/2005	SYSTEM, METHOD, AND APPARATUS FOR VARIABLE LENGTH DECODER	SANE, ANIRUDDHA
11051300	Not Issued	41	02/04/2005	Reprogramming of select registers in a linked list	SANE, ANIRUDDHA
60426504	Not Issued	159	11/15/2002	Algorithm for division coupled with truncation of signed binary numbers	SANE, ANIRUDDHA
60426583	Not Issued	159	11/15/2002	Dynamic booting of processor code memory using special "wait" instructions	SANE, ANIRUDDHA
60426618	Not Issued	159	11/15/2002	Algorithm for division coupled with rounding of signed binary numbers	SANE, ANIRUDDHA
60542594	Not Issued	159	02/05/2004	Reprogramming of select registers in a linked list	SANE, ANIRUDDHA

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = BHATIA

First Name = SANDEEP

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08625999</u>	Not Issued	161	04/01/1996	OXYGEN INJECTION IN NITRIC ACID PRODUCTION	BHATIA, SANDEEP
<u>08845849</u>	5882618	150	04/28/1997	PROCESS FOR CYANIC ACID PRODUCTION	BHATIA, SANDEEP
<u>08934842</u>	Not Issued	161	09/22/1997	METHOD FOR BUILDING A TESTING INFRASTRUCTURE FOR A SYSTEM ON A SEMICONDUCTOR CHIP	BHATIA, SANDEEP
<u>09151137</u>	Not Issued	161	09/10/1998	METHOD AND APPARATUS FOR PERFORMING TEST COMPACTION IN A SCAN ENVIRONMENT	BHATIA, SANDEEP
<u>09243129</u>	Not Issued	161	02/03/1999	OXYGEN INJECTION IN NITRIC ACID PRODUCTION	BHATIA, SANDEEP
<u>09641374</u>	6853385	150	08/18/2000	VIDEO, AUDIO AND GRAPHICS DECODE, COMPOSITE AND DISPLAY SYSTEM	BHATIA, SANDEEP
<u>09641930</u>	6975324	150	08/18/2000	VIDEO AND GRAPHICS SYSTEM WITH A VIDEO TRANSPORT PROCESSOR	BHATIA, SANDEEP
<u>09641936</u>	6636222	150	08/18/2000	VIDEO AND GRAPHICS SYSTEM WITH AN MPEG VIDEO DECODER FOR CONCURRENT MULTI-ROW DECODING	BHATIA, SANDEEP
<u>09717340</u>	Not Issued	161	11/22/2000	Oxygen injection in nitric acid production	BHATIA, SANDEEP
<u>09886408</u>	6617969	150	06/20/2001	EVENT NOTIFICATION SYSTEM	BHATIA, SANDEEP
<u>09951693</u>	Not Issued	120	09/12/2001	Command packets for personal video recorder	BHATIA, SANDEEP
<u>10045194</u>	6983017	150	10/19/2001	METHOD AND APPARATUS FOR IMPLEMENTING REDUCED MEMORY MODE FOR HIGH-DEFINITION TELEVISION	BHATIA, SANDEEP
<u>10060118</u>	Not Issued	41	01/29/2002	Error concealment for MPEG decoding with personal video recording functionality	BHATIA, SANDEEP
<u>10166824</u>	Not	80	06/11/2002	V-Chip data processing for decoder with	BHATIA, SANDEEP

	Issued			personal video recording functionality	
<u>10174355</u>	Not Issued	61	06/18/2002	Method of timebase management for MPEG decoding with personal video recording functionality	BHATIA, SANDEEP
<u>10174384</u>	Not Issued	41	06/18/2002	System and method for providing personal video recording trick modes	BHATIA, SANDEEP
<u>10317389</u>	7149248	150	12/11/2002	COMMAND PACKET SYSTEM AND METHOD SUPPORTING IMPROVED TRICK MODE PERFORMANCE IN VIDEO DECODING SYSTEMS	BHATIA, SANDEEP
<u>10317454</u>	Not Issued	30	12/11/2002	Caching system and method supporting improved trick mode performance in video decoding systems	BHATIA, SANDEEP
<u>10317642</u>	Not Issued	93	12/11/2002	SYSTEM AND METHOD FOR ENHANCING PERFORMANCE OF PERSONAL VIDEO RECORDING (PVR) FUNCTIONS ON HITS DIGITAL VIDEO STREAMS	BHATIA, SANDEEP
<u>10318295</u>	Not Issued	30	12/11/2002	Performing personal video recording (PVR) functions on digital video streams	BHATIA, SANDEEP
<u>10463170</u>	Not Issued	123	06/16/2003	Direct memory accessing for fetching macroblocks	BHATIA, SANDEEP
<u>10600162</u>	Not Issued	41	06/20/2003	System, method, and apparatus for simultaneously displaying multiple video streams	BHATIA, SANDEEP
<u>10600245</u>	Not Issued	41	06/26/2003	System, method, and apparatus for decoupling video decoder and display engine	BHATIA, SANDEEP
<u>10602387</u>	7307669	150	06/24/2003	SYSTEM, METHOD, AND APPARATUS FOR DISPLAYING STREAMS WITH DYNAMICALLY CHANGING FORMATS	BHATIA, SANDEEP
<u>10606216</u>	Not Issued	41	06/25/2003	Delta frame buffers	BHATIA, SANDEEP
<u>10606478</u>	Not Issued	41	06/26/2003	Buffer descriptor data structure for communication link between decode and display processes in MPEG decoders	BHATIA, SANDEEP
<u>10609918</u>	Not Issued	93	06/30/2003	DATA ALIGNMENT OF THE PACKETIZED ELEMENTARY STREAMS IN THE CODED DATA BUFFER FOR DUAL DECODE	BHATIA, SANDEEP
<u>10610071</u>	Not Issued	41	06/30/2003	System, method, and apparatus for displaying a plurality of video streams	BHATIA, SANDEEP
<u>10610149</u>	Not Issued	124	06/30/2003	PES data processing	BHATIA, SANDEEP
<u>10610246</u>	Not Issued	123	06/30/2003	PES data processing	BHATIA, SANDEEP
<u>10610247</u>	Not	61	06/30/2003	System, method, and apparatus for playing	BHATIA, SANDEEP

	Issued			back a plurality of video elementary streams with one playback channel	
10611451	Not Issued	41	06/30/2003	Scaling by early deinterlacing	BHATIA, SANDEEP
10623839	7277099	150	07/16/2003	VIDEO AND GRAPHICS SYSTEM WITH AN MPEG VIDEO DECODER FOR CONCURRENT MULTI-ROW DECODING	BHATIA, SANDEEP
10630537	Not Issued	71	07/29/2003	Test compaction using linear-matrix driven scan chains	BHATIA, SANDEEP
10706169	6839005	150	11/12/2003	LOW MEMORY AND MIPS EFFICIENT TECHNIQUE FOR DECODING HUFFMAN CODES USING MULTI-STAGE, MULTI-BITS LOOKUP AT DIFFERENT LEVELS	BHATIA, SANDEEP
10718445	Not Issued	71	11/19/2003	Dual scan chain design method and apparatus	BHATIA, SANDEEP
10735980	7284072	150	12/15/2003	DMA ENGINE FOR FETCHING WORDS IN REVERSE ORDER	BHATIA, SANDEEP
10736125	Not Issued	71	12/15/2003	DMA engine for fetching words in reverse bit order	BHATIA, SANDEEP
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	BHATIA, SANDEEP
10776760	Not Issued	93	02/10/2004	TWO ADDRESS MAP FOR TRANSACTIONS BETWEEN AN X-BIT PROCESSOR AND A Y-BIT WIDE MEMORY	BHATIA, SANDEEP
10791686	Not Issued	30	03/03/2004	Television functionality on a chip	BHATIA, SANDEEP
10874621	Not Issued	61	06/22/2004	Slow motion and high speed for digital video	BHATIA, SANDEEP
10874634	Not Issued	41	06/23/2004	Pause and freeze for digital video streams	BHATIA, SANDEEP
10892897	Not Issued	30	07/16/2004	System and method for audio/video synchronization	BHATIA, SANDEEP
10976446	Not Issued	30	10/29/2004	System and method for simultaneously scanning video for different size pictures	BHATIA, SANDEEP
10977864	Not Issued	40	10/29/2004	System, method, and apparatus for providing massively scaled down video using iconification	BHATIA, SANDEEP
10991143	Not Issued	61	11/17/2004	Providing power from a power storage unit in a first computing device to a second computing device	BHATIA, SANDEEP
10996159	7002494	150	11/23/2004	LOW MEMORY AND MIPS EFFICIENT TECHNIQUE FOR DECODING HUFFMAN CODES USING MULTI-STAGE, MULTI-BITS LOOKUP AT DIFFERENT LEVELS	BHATIA, SANDEEP

<u>10997652</u>	7110006	150	11/23/2004	VIDEO, AUDIO AND GRAPHICS DECODE, COMPOSITE AND DISPLAY SYSTEM	BHATIA, SANDEEP
<u>11017000</u>	Not Issued	30	12/20/2004	System and method for audio and visual synchronization	BHATIA, SANDEEP

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Inventor Name Search Result

Your Search was:

Last Name = AGARWAL
First Name = GAURAV

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10270264	Not Issued	94	10/11/2002	DEFICIT-BASED STRIPING ALGORITHM	AGARWAL, GAURAV
10358678	Not Issued	71	02/05/2003	Backpressure mechanism for switching fabric	AGARWAL, GAURAV
10775652	Not Issued	30	02/09/2004	Unified decoder architecture	AGARWAL, GAURAV
10964185	7260678	150	10/13/2004	SYSTEM AND METHOD FOR DETERMINING DISK OWNERSHIP MODEL	AGARWAL, GAURAV
11738561	Not Issued	30	04/23/2007	CHARACTERIZING TRANSMISSION OF DATA SEGMENTS WITHIN A SWITCH FABRIC USING MULTIPLE COUNTERS FOR EACH DESTINATION NODE	AGARWAL, GAURAV
11836487	Not Issued	20	08/09/2007	SYSTEM AND METHOD FOR DETERMINING DISK OWNERSHIP MODEL	AGARWAL, GAURAV
60362144	Not Issued	159	03/06/2002	Switch fabric architecture	AGARWAL, GAURAV
11950578	Not Issued	19	01/01/0001	METHOD OF TESTING MEMORY ARRAY AT OPERATIONAL SPEED USING SCAN	AGARWAL, GAURAV H.

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